

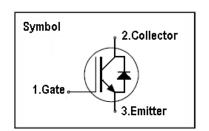
IGBT 内绝缘

Features

- 1200V 25A
- $\qquad \qquad V_{\text{CE(sat)(typ.)}} = 2.0 \text{V } @V_{\text{GE}} = 15 \text{V}, I_{\text{C}} = 25 \text{A}$
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA



JIAEN Trench IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating),UPS, general inverter and other soft switching applications.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	1200	V
V _{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
I.	Continuous Collector Current (T _C =25 °C)	50	А
lc	Continuous Collector Current (Tc=100°C)	25	А
Ісм	Pulsed Collector Current (Note 1)	75	А
l _F	Diode Continuous Forward Current (T _C =100 °C)	25	А
I _{FM}	Diode Maximum Forward Current (Note 1)	75	А
t _{sc}	Short Circuit Withstand Time	10	us
Б	Maximum Power Dissipation (T _C =25 °C)	130	W
P _D	Maximum Power Dissipation (Tc=100°C)	52	W
TJ	Operating Junction Temperature Range	-40 to +150	°C
T _{STG}	Storage Temperature Range	-50 to +150	℃

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	0.95	°C/ W
R _{th j-c}	Thermal Resistance, Junction to case for Diode	1.35	°C/ W
R _{th j-a}	Thermal Resistance, Junction to Ambient	40	°C/ W



Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	1200	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 1200V, V _{GE} = 0V	-	-	100	uA
	Gate Leakage Current, Forward	ge Current, Forward V_{GE} =30V, V_{CE} = 0V		-	100	nA
I _{GES}	Gate Leakage Current, Reverse	V_{GE} = -30V, V_{CE} = 0V	-	-	100	nA
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 250uA$	4.5	-	6.5	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V_{GE} =15V, I_{C} = 25A	-	2.0	2.5	V
Qg	Total Gate Charge	Vcc=600V	-	200		nC
Qge	Gate-Emitter Charge	V _{GE} =15V	-	15		nC
Qgc	Gate-Collector Charge	Ic=25A	-	80		nC
t _{d(on)}	Turn-on Delay Time		-	45	-	ns
t r	Turn-on Rise Time	Vcc=600V	-	60	-	ns
t d(off)	Turn-off Delay Time	V _{GE} =15V I _C =25A R _G =15Ω	-	180	-	ns
t f	Turn-off Fall Time		-	95	-	ns
Eon	Turn-on Switching Loss	Inductive Load	-	4.1	-	mJ
Eoff	Turn-off Switching Loss	Tc=25 ℃	-	0.8	-	mJ
Ets	Total Switching Loss]	-	4.9	-	mJ
C _{ies}	Input Capacitance	V _{CF} =25V	-	3600	-	pF
C _{oes}	Output Capacitance	V _{GE} =0V	-	120	-	pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz	-	65	-	pF

Electrical Characteristics of Diode (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =25A	1	2.2	2.8	V
trr	Diode Reverse Recovery Time	V _{CE} = 600V	1	260		ns
Irr	Diode peak Reverse Recovery Current	I _F = 25A	-	24		Α
Qrr	Diode Reverse Recovery Charge	dlF/dt = 200A/us	1	2730		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Performance Characteristics

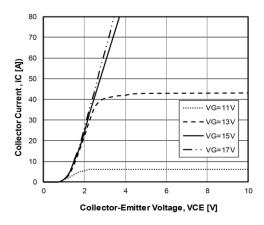


Fig 1. Output characteristics

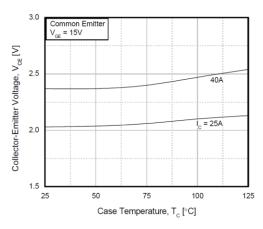


Figure 3. Saturation Voltage vs. Case Temperature at Variant Current Level

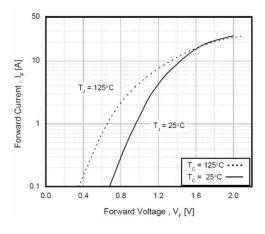


Figure 5. Forward Characteristics

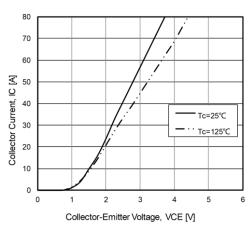


Fig 2. Typical Saturation Voltage Characteristics

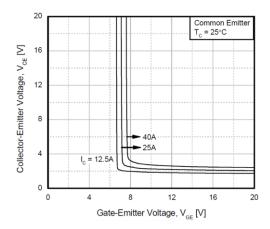


Figure 4. Saturation Voltage vs. VGE

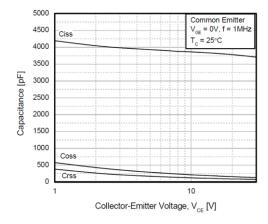


Figure 6. Capacitance Characteristics



Typical Performance Characteristics

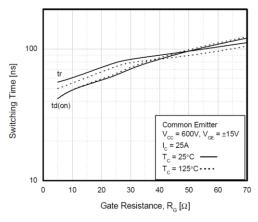


Figure 7. Turn-On Characteristics vs. Gate Resistance

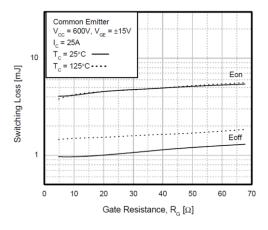


Figure 9. Switching Loss vs. Gate Resistance

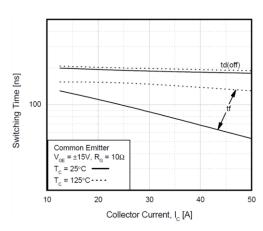


Figure 11. Turn-Off Characteristics vs. Collector Current

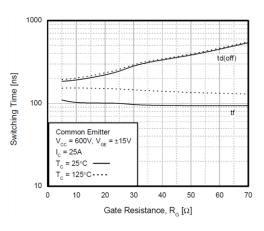


Figure 8. Turn-Off Characteristics vs. Gate Resistance

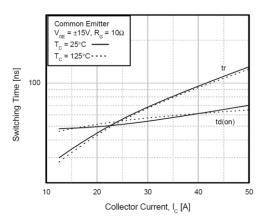


Figure 10. Turn-On Characteristics vs. Collector Current

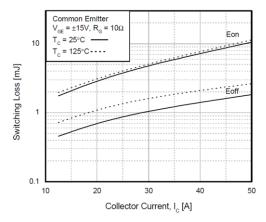


Figure 12. Switching Loss vs. Collector Current



Typical Performance Characteristics

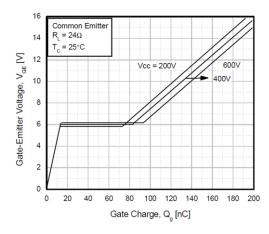


Figure 13. Gate Charge Characteristics

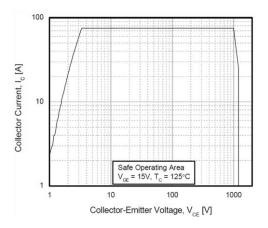


Figure 15. Turn-Off SOA

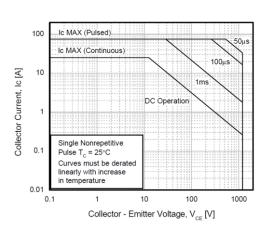


Figure 14. SOA Characteristics



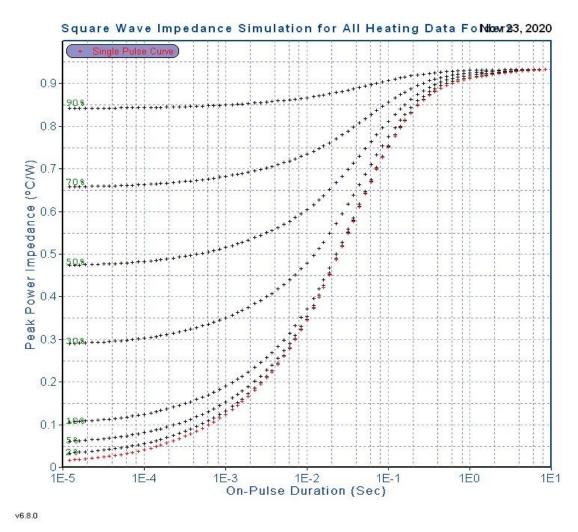


Figure 16.1 Transient Thermal Impedance of IGBT



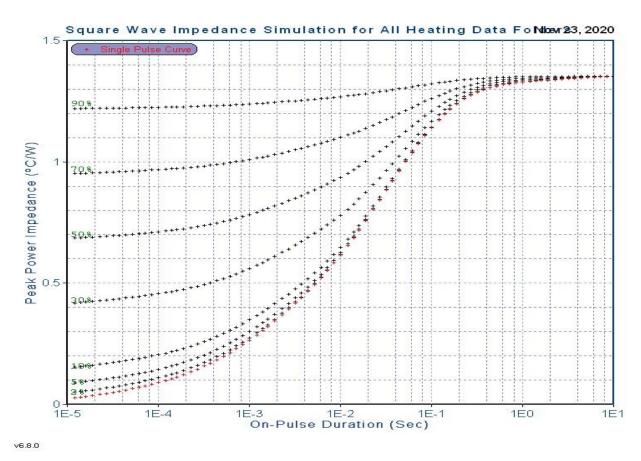
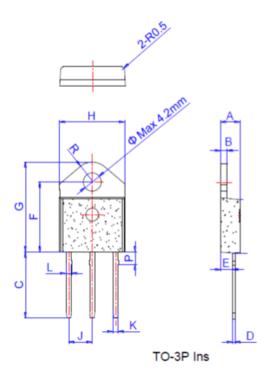


Figure 16.2 Transient Thermal Impedance of FRD



TO-3P-3L-II PACKAGE OUTLINE



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	4.40		4.60	0.173		0.181
В	1.45		1.55	0.057		0.061
С	14.35		15.60	0.565		0.614
D	0.50		0.70	0.020		0.028
E	2.70		2.90	0.106		0.114
F	15.80		16.50	0.622		0.650
G	20.40		21.10	0.803		0.831
Н	15.10		15.50	0.594		0.610
J	5.40		5.65	0.213		0.222
K	1.10		1.40	0.043		0.055
L	1.35		1.50	0.053		0.059
Р	2.80		3.00	0.110		0.118
R		4.35			0.171	



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