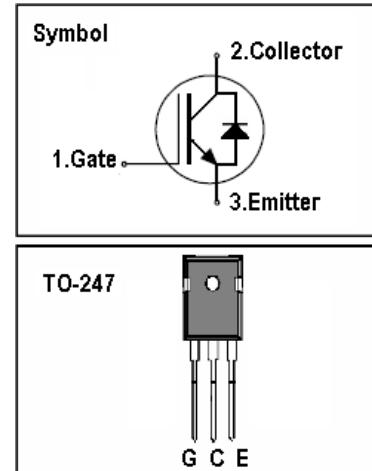


IGBT

Features

- 1200V 25A
- $V_{CE(sat)(typ.)}=2.0V$ @ $V_{GE}=15V, I_C=25A$
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA



General Description

JIAEN Trench IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating), UPS, general inverter and other soft switching applications.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	± 30	V
I_C	Continuous Collector Current ($T_C=25^\circ C$)	50	A
	Continuous Collector Current ($T_C=100^\circ C$)	25	A
I_{CM}	Pulsed Collector Current (Note 1)	75	A
I_F	Diode Continuous Forward Current ($T_C=100^\circ C$)	25	A
I_{FM}	Diode Maximum Forward Current (Note 1)	75	A
t_{sc}	Short Circuit Withstand Time	10	us
P_D	Maximum Power Dissipation ($T_C=25^\circ C$)	275	W
	Maximum Power Dissipation ($T_C=100^\circ C$)	110	W
T_J	Operating Junction Temperature Range	-40 to +155	°C
T_{STG}	Storage Temperature Range	-55 to +155	°C

Thermal Characteristics

Symbol	Parameter	Max.	Units
$R_{th\ j-c}$	Thermal Resistance, Junction to case for IGBT	0.45	°C/W
$R_{th\ j-c}$	Thermal Resistance, Junction to case for Diode	1.5	°C/W
$R_{th\ j-a}$	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{CES}	Collector-Emitter Breakdown Voltage	$V_{GE}=0V, I_C=250\mu A$	1200	-	-	V
I_{CES}	Collector-Emitter Leakage Current	$V_{CE}=1200V, V_{GE}=0V$	-	-	100	μA
I_{GES}	Gate Leakage Current, Forward	$V_{GE}=30V, V_{CE}=0V$	-	-	100	nA
	Gate Leakage Current, Reverse	$V_{GE}=-30V, V_{CE}=0V$	-	-	100	nA
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE}=V_{CE}, I_C=250\mu A$	4.5	-	6.5	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE}=15V, I_C=25A$	-	2.0	2.5	V
Q_g	Total Gate Charge	$V_{CC}=600V$ $V_{GE}=15V$ $I_C=25A$	-	200		nC
Q_{ge}	Gate-Emitter Charge		-	15		nC
Q_{gc}	Gate-Collector Charge		-	80		nC
$t_{d(on)}$	Turn-on Delay Time	$V_{CC}=600V$ $V_{GE}=15V$ $I_C=25A$ $R_G=15\Omega$ Inductive Load $T_c=25^\circ C$	-	45	-	ns
t_r	Turn-on Rise Time		-	60	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	180	-	ns
t_f	Turn-off Fall Time		-	95	-	ns
E_{on}	Turn-on Switching Loss		-	4.1	-	mJ
E_{off}	Turn-off Switching Loss		-	0.8	-	mJ
E_{ts}	Total Switching Loss		-	4.9	-	mJ
C_{ies}	Input Capacitance	$V_{CE}=25V$ $V_{GE}=0V$ $f = 1MHz$	-	3600	-	pF
C_{oes}	Output Capacitance		-	120	-	pF
C_{res}	Reverse Transfer Capacitance		-	65	-	pF

Electrical Characteristics of Diode ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_F	Diode Forward Voltage	$I_F=25A$	-	2.2	2.8	V
t_{rr}	Diode Reverse Recovery Time	$V_{CE} = 600V$ $I_F = 25A$ $dI_F/dt = 200A/us$	-	260		ns
I_{rr}	Diode peak Reverse Recovery Current		-	24		A
Q_{rr}	Diode Reverse Recovery Charge		-	2730		nC

Notes:

- Repetitive Rating: Pulse width limited by maximum junction temperature



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Typical Performance Characteristics

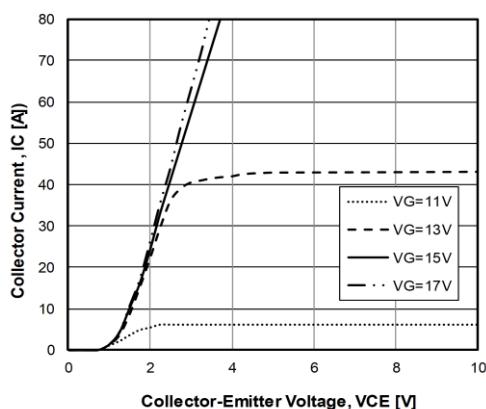


Fig 1. Output characteristics

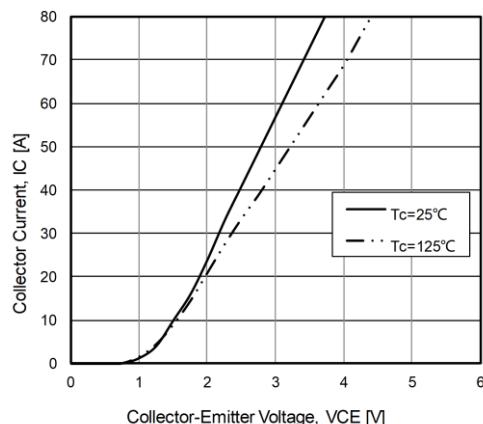


Fig 2. Typical Saturation Voltage Characteristics

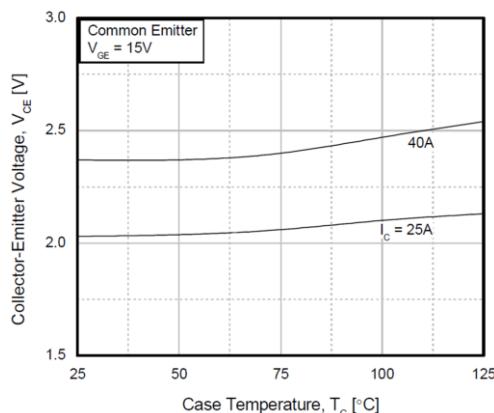


Figure 3. Saturation Voltage vs. Case Temperature at Variant Current Level

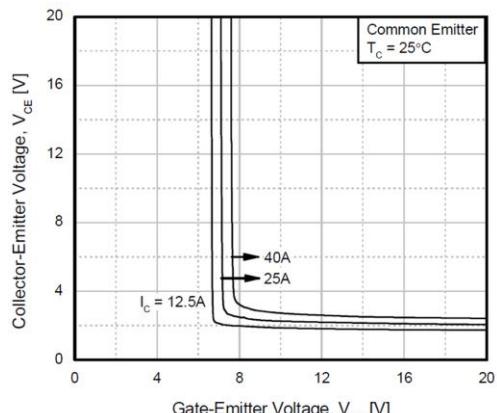


Figure 4. Saturation Voltage vs. VGE

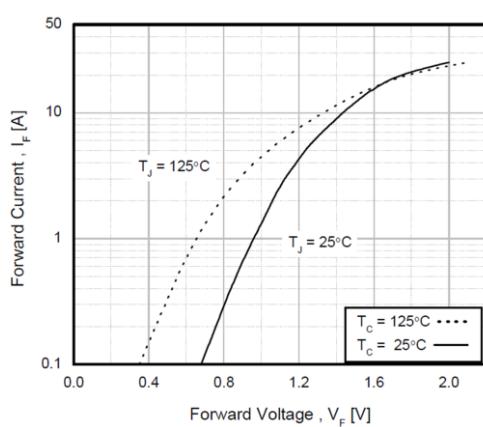


Figure 5. Forward Characteristics

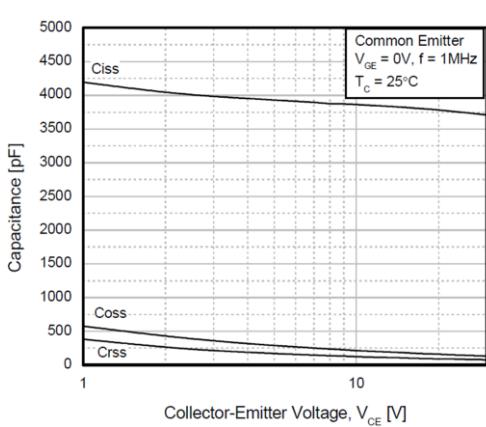


Figure 6. Capacitance Characteristics



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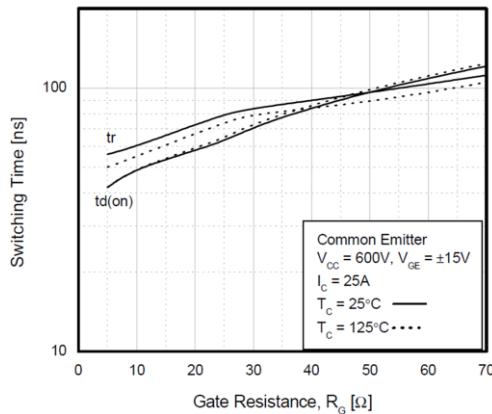


Figure 7. Turn-On Characteristics vs. Gate Resistance

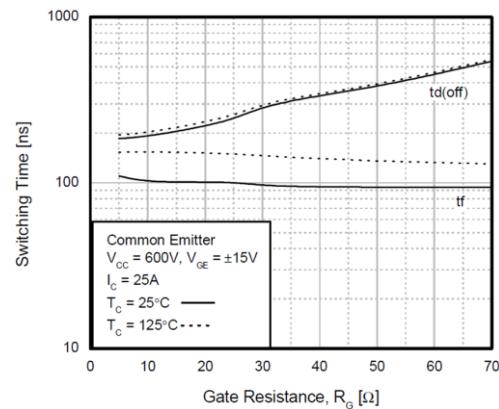


Figure 8. Turn-Off Characteristics vs. Gate Resistance

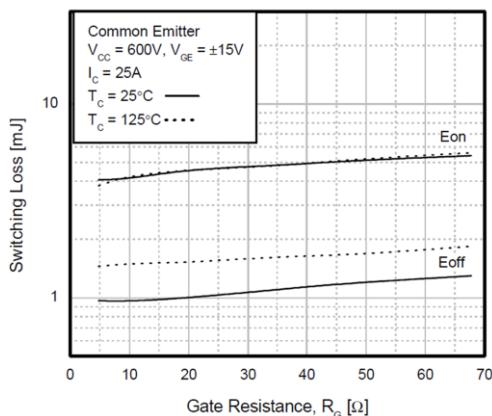


Figure 9. Switching Loss vs. Gate Resistance

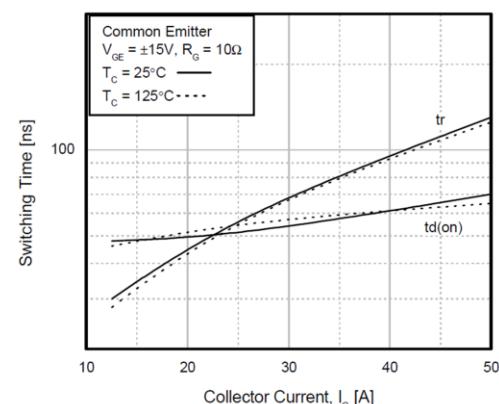


Figure 10. Turn-On Characteristics vs. Collector Current

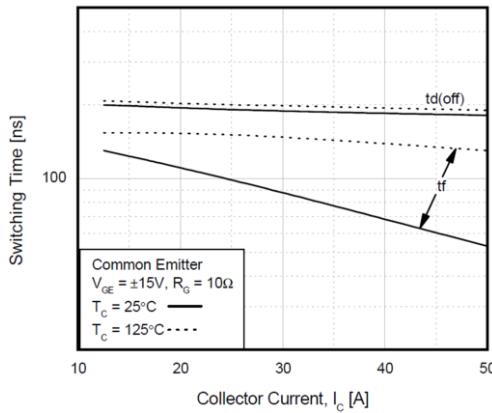


Figure 11. Turn-Off Characteristics vs. Collector Current

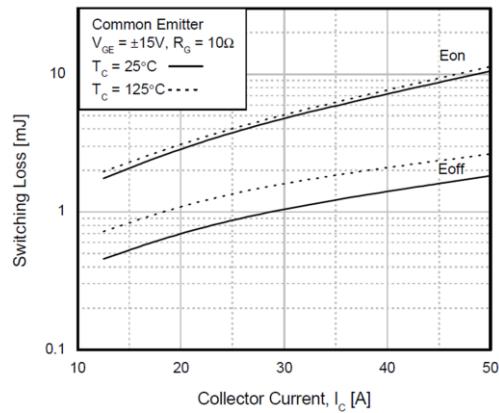


Figure 12. Switching Loss vs. Collector Current



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Typical Performance Characteristics

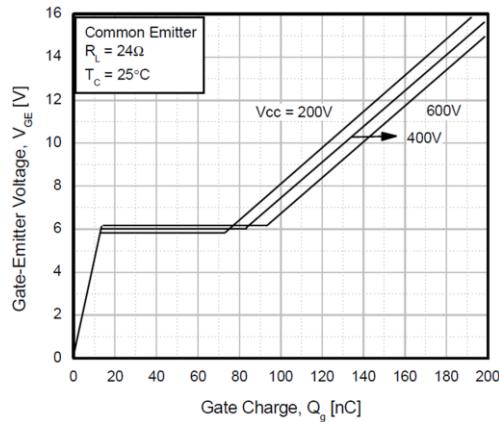


Figure 13. Gate Charge Characteristics

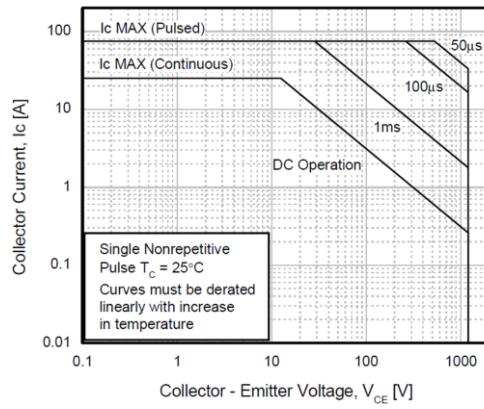


Figure 14. SOA Characteristics

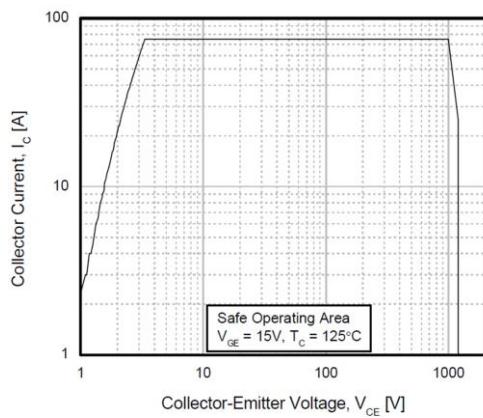


Figure 15. Turn-Off SOA

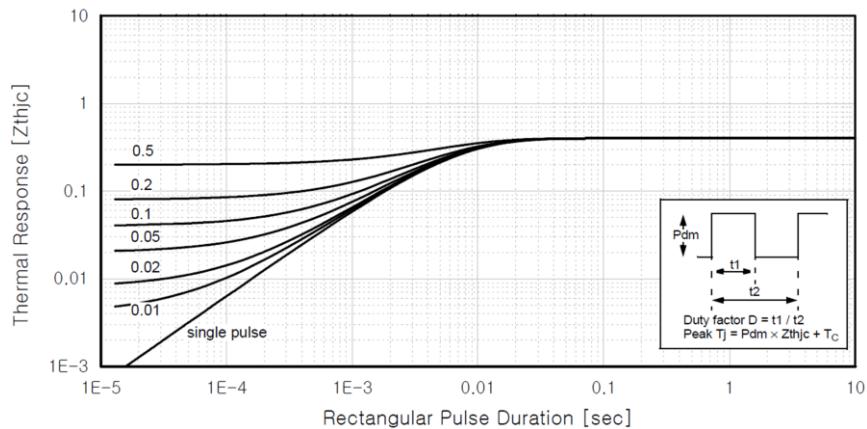


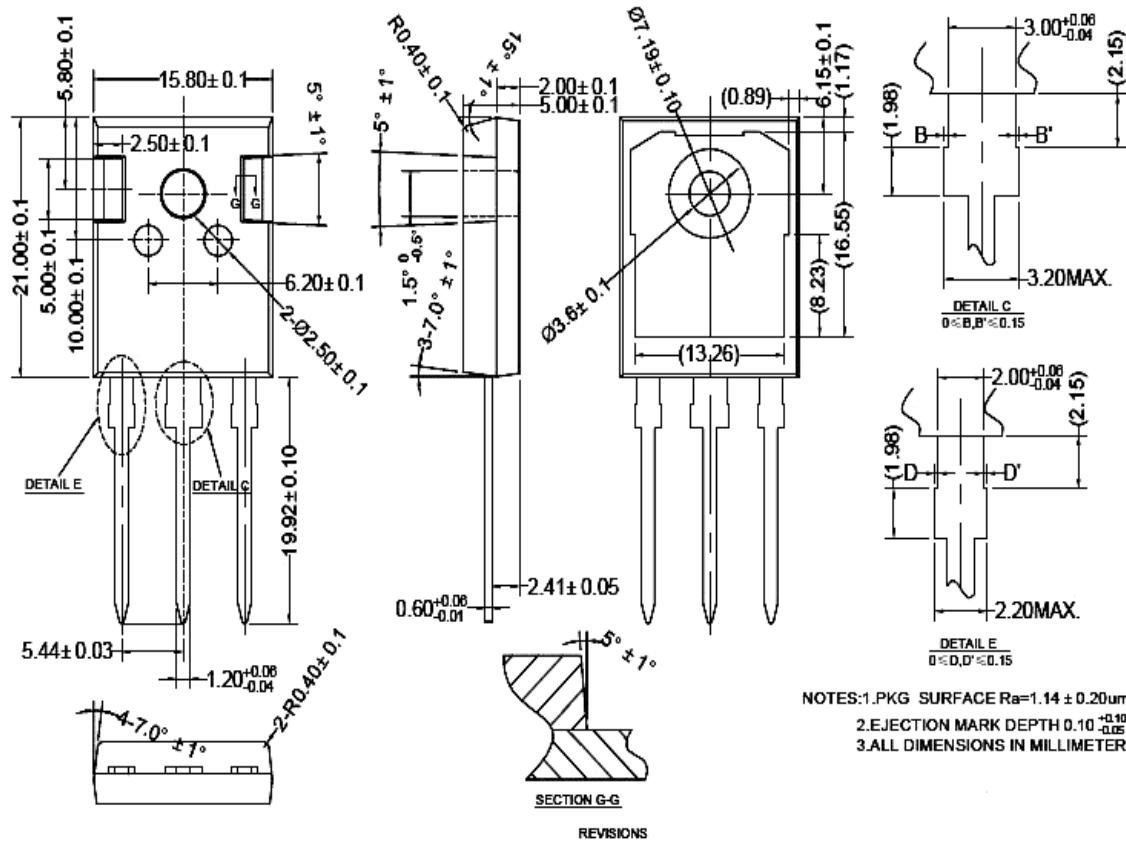
Figure 16. Transient Thermal Impedance of IGBT



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TO247 PACKAGE OUTLINE



公差标注	公差值	表面粗糙度
0	±0.2	Ra3.2~6.3
0.0	±0.1	Ra1.6~3.2
0.00	±0.01	Ra0.8~1.6
0.000	±0.005	Ra0.4~0.8
0.0000	±0.002	Ra0.2~0.4

NOTES:1.PKG SURFACE Ra=1.14 ± 0.20μm.
2.EJECTION MARK DEPTH 0.10 ^{+0.10}_{-0.05}.
3.ALL DIMENSIONS IN MILLIMETERS.

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